

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(nonvolatile and semiconductor and memory and trench\$2 and gate and insulating and floating and electrode and groove and control and width and thickness and "1.6" and times). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/29 11:11